BS7-KZT3

Bs7 Option Switch Settings: 1 (Pulse/ICT004/MDB)

Supported bill KZT 200, 500, 1000

3bills.

Bs7 dip-switch settings and functions:

	FUNCTION	SW1	SW2	sw3	SW4	SW5	SW6	SW7	SW8
	Reject KZT 200	ON							
*	Accept KZT 200	OFF							
	Reject KZT 500		ON						
*	Accept KZT 500		OFF						
	Reject KZT 1000			ON					
*	Accept KZT 1000			OFF					
	Reserved				ON				
*	Reserved				OFF				
	Reserved					ON			
*	Reserved					OFF			
	Reserved						ON		
*	Reserved						OFF		
	Harness Disable							ON	
*	Harness Engble							OFF	
	Inhiblit Active High		•			•		•	ON
*	Inhibit Active Low								OFF

Factory configuration has all switches in the OFF position.

V7 Interface Settings: 2 (Pulse)

	INTERFACE	SW1	SW2	SW3	SW4
*	1 pulse / KZT 100	OFF	OFF		
	2 pulse / KZT 100	ON	OFF		
	4 pulse / KZT 100	OFF	ON		
	20 pulse / KZT 100	ON	ON		
*	50ms LO / 50ms HI			OFF	OFF
	60ms LO / 300ms HI			ON	OFF
	30ms LO / 50ms HI			OFF	ON
	150ms LO / 150ms HI			ON	ON

[★] Manufacture setting

BS7-KZT3(Pulse/ICT004/MDB)

V7 Interface Settings: 3 (ICT) V7 Interface Settings: 3 (Pulse)

	INTERFACE	SW1	SW2	SW3	SW4
	Reserved	ON			
*	Reserved	OFF			
	Pulse Mode		ON	OFF	
	MDB Interface		OFF	OFF	
*	ICT Interface		OFF	ON	
	Reserved		ON	ON	
	Reserved				ON
*	1 AGSGI VEU				OFF

	INTERFACE	SW1	SW2	SW3	SW4
*	Credit-Pulse Normal High	ON			
	Credit-Pulse Normal Low	OFF			
*	Pulse Mode		ON	OFF	
	MDB Interface		OFF	OFF	
	ICT Interface		OFF	ON	
	Reserved		ON	ON	
	Reserved				ON
*	i Nesel Veu				OFF

V7 Interface Settings: 3 (MDB)

	INTERFACE	SW1	SW2	SW3	SW4
*	Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2	ON			
	Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0	OFF			
	Pulse Mode		ON	OFF	
*	MDB Interface		OFF	OFF	
	ICT Interface		OFF	ON	
	Reserved		ON	ON	
	Reserved				ON
*	Reserved				OFF

Currency Assign Data

Interface Bill value	ICT	Pulse	MDB
BV1	KZT 200	KZT 200	KZT 200
BV2	KZT 500	KZT 500	KZT 500
BV3	KZT 1000	KZT 1000	KZT 1000

Note: 1.Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.

2.Remove the front cover and the dip-switch in on the CPU board.

(Please refer to "Sw1" setting.)