

## L77F/PL77-AMD2

### L77F/PL77 DIP SWITCHES SETTING : 1(Pulse/ICT104U/ICT104V/MDB)

Supported bill AMD 1000, 2000 2bills.

| FUNCTION                                    | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 | SW9 | SW10 |
|---------------------------------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|------|
| ★ 1 Pulse / AMD 1000                        | OFF | OFF | OFF |     |     |     |     |     |     |      |
| 2 Pulse / AMD 1000                          | OFF | OFF | ON  |     |     |     |     |     |     |      |
| 3 Pulse / AMD 1000                          | OFF | ON  | OFF |     |     |     |     |     |     |      |
| 4 Pulse / AMD 1000                          | OFF | ON  | ON  |     |     |     |     |     |     |      |
| 5 Pulse / AMD 1000                          | ON  | OFF | OFF |     |     |     |     |     |     |      |
| 10 Pulse / AMD 1000                         | ON  | OFF | ON  |     |     |     |     |     |     |      |
| 20 Pulse / AMD 1000                         | ON  | ON  | OFF |     |     |     |     |     |     |      |
| 100 Pulse / AMD 1000                        | ON  | ON  | ON  |     |     |     |     |     |     |      |
| Fast output Pulse<br>Lo= 50ms<br>Hi=100ms   |     |     |     | ON  |     |     |     |     |     |      |
| ★ Slow output Pulse<br>Lo= 50ms<br>Hi=300ms |     |     |     | OFF |     |     |     |     |     |      |
| Inhibit level Active Low                    |     |     |     |     | ON  |     |     |     |     |      |
| ★ Inhibit level Active High                 |     |     |     |     | OFF |     |     |     |     |      |
| ★ Accept AMD 1000                           |     |     |     |     |     | ON  |     |     |     |      |
| Reject AMD 1000                             |     |     |     |     |     | OFF |     |     |     |      |
| ★ Accept AMD 2000                           |     |     |     |     |     |     | ON  |     |     |      |
| Reject AMD 2000                             |     |     |     |     |     |     | OFF |     |     |      |
| ★ Reserved                                  |     |     |     |     |     |     |     | ON  |     |      |
| Reserved                                    |     |     |     |     |     |     |     | OFF |     |      |
| ★ Reserved                                  |     |     |     |     |     |     |     |     | ON  |      |
| Reserved                                    |     |     |     |     |     |     |     |     | OFF |      |
| ★ Reserved                                  |     |     |     |     |     |     |     |     |     | ON   |
| Reserved                                    |     |     |     |     |     |     |     |     |     | OFF  |

★ Manufacture setting

Note : Dip switches 1 to 5 are only used for pulse protocol.

## L77F&PL77-AMD2(Pulse/ICT104U/ICT104V/MDB)

### DIP SWITCHES SETTING : 2(Pulse)

| FUNCTION            | SW1 | SW2 | SW3 | SW4 |
|---------------------|-----|-----|-----|-----|
| ★ Pulse Normal High | ON  |     |     |     |
| Pulse Normal Low    | OFF |     |     |     |
| ★ Pulse Mode        |     | ON  | OFF |     |
| ICT104U Mode        |     | OFF | ON  |     |
| MDB Mode            |     | ON  | ON  |     |
| ICT104V Mode        |     | OFF | OFF |     |
| Reserved            |     |     |     | ON  |
| ★                   |     |     |     | OFF |

★ Manufacture setting

### DIP SWITCHES SETTING : 2(ICT104U)

| FUNCTION       | SW1 | SW2 | SW3 | SW4 |
|----------------|-----|-----|-----|-----|
| ★ Reserved     | ON  |     |     |     |
| Reserved       | OFF |     |     |     |
| Pulse Mode     |     | ON  | OFF |     |
| ★ ICT104U Mode |     | OFF | ON  |     |
| MDB Mode       |     | ON  | ON  |     |
| ICT104V Mode   |     | OFF | OFF |     |
| Reserved       |     |     |     | ON  |
| ★              |     |     |     | OFF |

★ Manufacture setting

### Currency Assign Data

| Interface<br>Bill value | ICT104U  | ICT104V  | Pulse    | MDB      |
|-------------------------|----------|----------|----------|----------|
| BV1                     | AMD 1000 | AMD 1000 | AMD 1000 | AMD 1000 |
| BV2                     | AMD 2000 | AMD 2000 | AMD 2000 | AMD 2000 |

### DIP SWITCHES SETTING : 2(ICT104V)

| FUNCTION       | SW1 | SW2 | SW3 | SW4 |
|----------------|-----|-----|-----|-----|
| ★ Reserved     | ON  |     |     |     |
| Reserved       | OFF |     |     |     |
| Pulse Mode     |     | ON  | OFF |     |
| ICT104U Mode   |     | OFF | ON  |     |
| MDB Mode       |     | ON  | ON  |     |
| ★ ICT104V Mode |     | OFF | OFF |     |
| Reserved       |     |     |     | ON  |
| ★              |     |     |     | OFF |

★ Manufacture setting

### DIP SWITCHES SETTING : 2(MDB)

| FUNCTION                                                        | SW1 | SW2 | SW3 | SW4 |
|-----------------------------------------------------------------|-----|-----|-----|-----|
| ★ Scaling Factor (SF) = 100<br>Decimal Point Position (DPP) = 2 | ON  |     |     |     |
| Scaling Factor (SF) = 1<br>Decimal Point Position (DPP) = 0     | OFF |     |     |     |
| Pulse Mode                                                      |     | ON  | OFF |     |
| ICT104U Mode                                                    |     | OFF | ON  |     |
| ★ MDB Mode                                                      |     | ON  | ON  |     |
| ICT104V Mode                                                    |     | OFF | OFF |     |
| Reserved                                                        |     |     |     | ON  |
| ★                                                               |     |     |     | OFF |

★ Manufacture setting