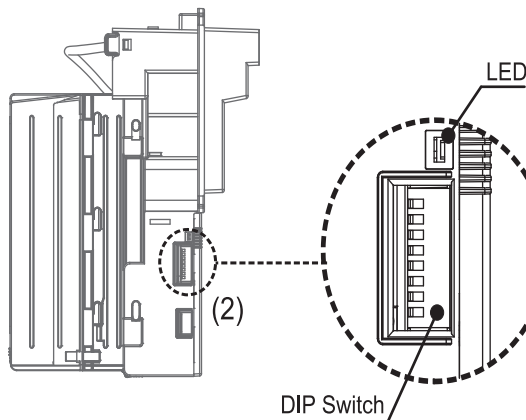


XBA-UAH10 Option Switch Settings

Supported bill UAH 1, 2, 5, 10, 20, 50, 100, 200, 500, 1000 10bills.

	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
★	Reject H 1 & 2	ON							
★	Accept H 1 & 2	OFF							
★	Reject H 5 & 10		ON						
★	Accept H 5 & 10		OFF						
★	Reject H 20 & 50			ON					
★	Accept H 20 & 50			OFF					
★	Reject H 100 & 200				ON				
★	Accept H 100 & 200				OFF				
★	Reject H 500 & 1000					ON			
★	Accept H 500 & 1000					OFF			
★	Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
★	Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
★	Stack Banknote when Power-up							ON	
★	Reject Banknote when Power-up							OFF	
	Reserved								ON
★	Reserved								OFF

This dip switch is located at the side of XBA.



Currency Assign Data

Interface Bill value	ICT104U	JPSTD	ccTalk	Pulse	RS232 A0
BV1	H 1	H 1	H 1	H 1	H 1
BV2	H 2		H 2	H 2	H 2
BV3	H 5		H 5	H 5	H 5
BV4	H 10		H 10	H 10	H 10
BV5	H 20		H 20	H 20	H 20
BV6	H 50		H 50	H 50	H 50
BV7	H 100		H 100	H 100	H 100
BV8	H 200		H 200	H 200	H 200
BV9	H 500		H 500	H 500	H 500
BV10	H 1000		H 1000	H 1000	H 1000

RS232 A0 INFORMATION

BillType Data	
Code	Denomination
1	H 1
2	H 2
3	H 5
4	H 10
5	H 20
6	H 50
7	H 100
8	H 200
9	H 500
10	H 1000

Appendix

XBA-UAH10(Pulse/JPSTD/ccTalk/ICT104U/RS232 A0)

FUNCTION		SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
★	Pulse Mode	Other Mode							
	Inhibit Active HIGH	ON							
	Inhibit Active LOW	OFF							
	Reserved		ON						
★	Reserved		OFF						
★	50ms LO / 50ms HI			OFF	OFF				
	50ms LO / 100ms HI			OFF	ON				
	65ms LO / 75ms HI			ON	OFF				
	100ms LO / 150ms HI			ON	ON				
★	Credit-Pulse Normal HIGH	ccTalk 16-bit Encryption mode & Restoe Mode.(See Table.1)				ON			
	Credit-Pulse Normal LOW	ccTalk 16-bit Decrypt mode & 8-bit checksum.(See Table.1)				OFF			
★	Pulse Mode	Pulse Mode					ON		
	Other Mode	Other Mode					OFF		
★	1 pulse / H 1	ccTalk Mode						OFF	OFF
	5 pulses / H 1	RS232 A0 Mode						OFF	ON
	10 pulses / H 1	ICT104U Mode						ON	OFF
	25 pulses / H 1	JPSTD Mode						ON	ON

★ Manufacture setting

This dip switch is located on the CPU board, remove the CPU board cover first.

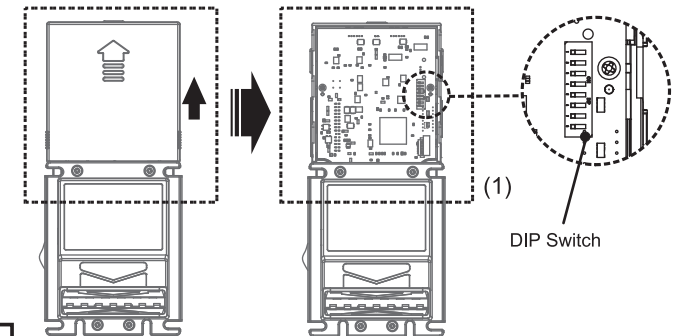


Table 1

	SW1	SW5
8-Bit Checksum	OFF	OFF
Resore mode(Note.1)	OFF	ON
CRC 16-Bit Decrypt Checksum	ON	OFF
CRC 16-Bit Encryption Checksum	ON	ON

Note : 1.After setting dip switch of the credit pulses, you should reset the bill acceptor again.
2.ccTalk address to its default value of 40 and the encryption key to its default value of 123456.
3.Calibration card is needed.