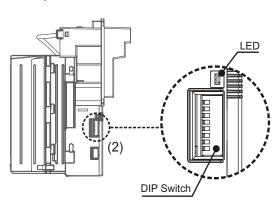
XBA-UAH10 Option Switch Settings

Supported bill UAH 1, 2, 5, 10, 20, 50, 100, 200, 500, 1000 10bills.

	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
	Reject H 1 & 2	ON							
*	Accept H 1 & 2	OFF							
	Reject H 5 & 10		ON						
*	Accept H 5 & 10		OFF						
	Reject H 20 & 50			ON					
*	Accept H 20 & 50			OFF					
	Reject H 100 & 200				ON				
*	Accept H 100 & 200				OFF				
	Reject H 500 & 1000					ON			
*	Accept H 500 & 1000					OFF			
	Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
*	Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
	Stack Banknote when Power-up								
*	Reject Banknote when Power-up	OFF							
	Reserved								ON
*	Reserved								OFF

This dip switch is located at the side of XBA.



Currency Assign Data

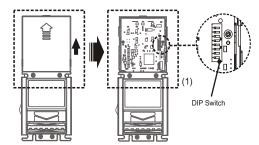
Interface	ICT104U	GPT	ICT104V	Pu	lse	MDB	
Bill value	1011040	GFI	1011041	When Dip 1 is ON	When Dip 1 is OFF	When Dip 1 is ON	When Dip 1 is OFF
BV1	1	1	1		1	5	1
BV2	2	2	2		2	10	2
BV3	5	5	5	5	5	20	5
BV4	10	10	10	10	10	50	10
BV5	20		20	20	20	100	20
BV6	50		50	50	50	200	50
BV7	100		100	100	100	500	100
BV8	200		200	200	200	1000	200
BV9	500		500	500	500		
BV10	1000		1000	1000	1000		

Appendix

XBA-UAH10 (Pulse/MDB/ICT104U/GPT/ICT104V)

	FUNCTION	SW1	SW2	SW3	SW4		SW5		SW6	SW7	SW8
	Inhibit Active HIGH	ON									
*	Inhibit Active LOW	OFF									
	ENABLE INHIBIT-Sleep Only		ON								
*	5 second Timeout Only		OFF								
*	NORMAL			OFF	OFF						
	MODE1			OFF	ON						
	MODE2			ON	OFF						
	MODE1+2			ON	ON						
	Pulse Mode	MDB Mode ICT1					04V Mode				
*	Credit-Pulse Normal HIGH	Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2 Connect v					with coin changer	ON			
	Credit-Pulse Normal LOW	Scaling Factor (SF) = 1 Connect					t with ICT VCCS verter board	OFF			
*	Pulse Mode								ON		
	Other Mode								OFF		
		Other Mode									
*	1 pulse / H 1(Dip	o 1: OFF); H 5(Dip 1: ON)					MDB Mode			OFF	OFF
	5 pulse / H 1(Dip	1(Dip 1: OFF); H 5(Dip 1: ON)					GPT Mode			OFF	ON
	10 pulse / H 1(Dip 1: OFF);H 5(Dip 1: ON)						ICT104U Mode			ON	OFF
	25 pulse / H 1(Dip 1: OFF) ; H 5(Dip 1: ON) ICT							ICT104V Mode			ON

This dip switch is located on the CPU board. remove the CPU board cover first.



★ Manufacture setting

After setting dip switch of the credit pulses, you should reset the bill acceptor again.

Note: 1.Please reset the bill acceptor after set

the dip switch.

- 2.Dip switches 1 to 5 are only used for pulse protocol. 3.calibrate card.
- 4.Pulse Mode:

When Dip 1 is OFF,One Pulse per 1 H When Dip 1 is ON,One Pulse per 5 H MDB Mode:

When Dip 1 is OFF, Accept 1 H.
When Dip 1 is ON, Accept 1000 H.

5.NORMAL: No sleep function
MODE 1: Bill-acceptor wakes up VMC with
banknote only (special cable required)
MODE 2: VMC wakes up bill-acceptor MODE 1+2: Bill-acceptor wakes up with banknote or by VMC

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