

V7-SAR4

V7 Option Switch Settings: 1 (Pulse/ICT004U/MDB)

Supported bill SAR 1, 5, 10, 50 4bills.

V7 dip-switch settings and functions:

	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
★	Reject SAR 1	ON							
	Accept SAR 1	OFF							
	Reject SAR 5		ON						
★	Accept SAR 5		OFF						
	Reject SAR 10			ON					
★	Accept SAR 10			OFF					
	Reject SAR 50				ON				
★	Accept SAR 50				OFF				
	Reserved					ON			
★	Reserved					OFF			
	Reserved						ON		
★	Reserved						OFF		
	Harness Disable							ON	
★	Harness Enable							OFF	
	Inhibit Active High								ON
★	Inhibit Active Low								OFF

◆ Factory configuration has all switches in the OFF position.

V7 Interface Settings: 2 (Pulse)

	INTERFACE	SW1	SW2	SW3	SW4
★	1 pulse / SAR 1	OFF	OFF		
	2 pulse / SAR 1	ON	OFF		
	4 pulse / SAR 1	OFF	ON		
	20 pulse / SAR 1	ON	ON		
★	50ms LO / 50ms HI			OFF	OFF
	60ms LO / 300ms HI			ON	OFF
	30ms LO / 50ms HI			OFF	ON
	150ms LO / 150ms HI			ON	ON

★ Manufacture setting

V7-SAR4(Pulse/ICT004U/MDB)

V7 Interface Settings: 3 (ICT004U)

	INTERFACE	SW1	SW2	SW3	SW4
★	Reserved	ON			
		OFF			
	Pulse Mode		ON	OFF	
	MDB Interface		OFF	OFF	
★	ICT004U Interface		OFF	ON	
	Reserved		ON	ON	
★	Reserved				ON
					OFF

V7 Interface Settings: 3 (Pulse)

	INTERFACE	SW1	SW2	SW3	SW4
★	Credit-Pulse Normal High	ON			
	Credit-Pulse Normal Low	OFF			
★	Pulse Mode		ON	OFF	
	MDB Interface		OFF	OFF	
	ICT004U Interface		OFF	ON	
	Reserved		ON	ON	
★	Reserved				ON
					OFF

V7 Interface Settings: 3 (MDB)

	INTERFACE	SW1	SW2	SW3	SW4
★	Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2	ON			
	Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0	OFF			
	Pulse Mode		ON	OFF	
★	MDB Interface		OFF	OFF	
	ICT004U Interface		OFF	ON	
	Reserved		ON	ON	
★	Reserved				ON
					OFF

Currency Assign Data

Interface Bill value	ICT004U	Pulse	MDB
BV1	SAR 1	SAR 1	SAR 1
BV2	SAR 5	SAR 5	SAR 5
BV3	SAR 10	SAR 10	SAR 10
BV5	SAR 50	SAR 50	SAR 50

Note: 1.Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.
2.Remove the front cover and the dip-switch in on the CPU board.
(Please refer to "Sw1" setting.)