#### V77E-TRY6

# V77E Option Switch Settings: 1(Pulse/ICT104V/ICT104U/MDB)

Supported bill TRY 5, 10, 20, 50, 100, 200 6bills.

V77E dip-switch settings and functions:

	<u> </u>												
	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW1	SW2	SW3	SW4
	Reject TRY 5	ON											
*	Accept TRY 5	OFF											
	Reject TRY 10		ON										
*	Accept TRY 10		OFF										
	Reject TRY 20			ON									
*	Accept TRY 20			OFF									
	Reject TRY 50				ON								
*	Accept TRY 50				OFF								
	Reject TRY 100 & 200				•	ON							
*	Accept TRY 100 & 200					OFF							
	Reserved						ON						
*	Reserved						OFF						
	Pulse Mode			Other	Mode		•						
	Harness Disable	В	A stops	worki	ng whe	n fishir	ng	ON					
*	Harness Enable			Nor	mal			OFF					
	Inhibit Active High								ON				
*	Inhibit Active Low								OFF				
*	1 pulse / TRY 5									OFF	OFF		
	5 pulses / TRY 5									ON	OFF		
	10 pulses / TRY 5									OFF	ON		
	25 pulses / TRY 5									ON	ON		
*		5	0ms L0	0 / 50	ms HI							OFF	OFF
	Pulse Speed	6	0ms L0	O / 300	ms HI							ON	OFF
	ruise opeeu	3	0ms L0	D / 50	ms HI							OFF	ON
		15	0ms L0	D / 150	ms HI							ON	ON

#### ★ Manufacture setting

Note: (1) Default setting.

- (2) Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.
- (3) Remove the front cover and the dip-switch in on the CPU board.

### Appendix

# V77E-TRY6(Pulse/ICT104V/ICT104U/MDB)

Interface Settings: 2(Pulse) Currency Assign Data

	INTERFACE	SW1	SW2	SW3	SW4
*	Credit-Pulse Normal HIGH	ON			
	Credit-Pulse Normal LOW	OFF			
*	Pulse Interface		ON	OFF	
	ICT104V Interface		OFF	ON	
	MDB Interface		ON	ON	
	ICT104U Interface		OFF	OFF	
	Reserved				ON
×	Reserved				OFF

Interface Bill value	ICT104U		ICT104V		Pulse		MDB	
BV1	TRY	5	TRY	5	TRY	5	TRY	5
BV2	TRY	10	TRY	10	TRY	10	TRY	10
BV3	TRY	20	TRY	20	TRY	20	TRY	20
BV4	TRY	50	TRY	50	TRY	50	TRY	50
BV5	TRY '	100	TRY	100	TRY	100	TRY	100
BV6	TRY 200		TRY 200		TRY 200		TRY 200	

### Interface Settings: 2(ICT104V) Interface Settings: 2(ICT104U)

	INTERFACE	SW1	SW2	SW3	SW4
*	Reserved	ON			
	Reserved	OFF			
	Pulse Interface		ON	OFF	
*	ICT104V Interface		OFF	ON	
	MDB Interface		ON	ON	
	ICT104U Interface		OFF	OFF	
	Reserved				ON
*	176961760				OFF

INTERFACE	SW1	SW2	SW3	SW4
Dogoryod	ON			
Reserved	OFF			
Pulse Interface		ON	OFF	
ICT104V Interface		OFF	ON	
MDB Interface		ON	ON	
ICT104U Interface		OFF	OFF	
Dogoryod				ON
reserved				OFF
	Reserved Pulse Interface ICT104V Interface MDB Interface	Reserved ON OFF Pulse Interface ICT104V Interface MDB Interface ICT104U Interface	Reserved         ON OFF           Pulse Interface         ON           ICT104V Interface         OFF           MDB Interface         ON           ICT104U Interface         OFF	Reserved         ON OFF           Pulse Interface         ON OFF           ICT104V Interface         OFF ON ON ON ON ON OFF           ICT104U Interface         OFF OFF

## **Interface Settings: 2(MDB)**

	INTERFACE	SW1	SW2	SW3	SW4
*	Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2	ON			
	Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0	OFF			
	Pulse Interface		ON	OFF	
	ICT104V Interface		OFF	ON	
*	MDB Interface		ON	ON	
	ICT104U Interface		OFF	OFF	
<u>,</u>	Reserved				ON
	i vesel ved		OFF		

<sup>★</sup> Manufacture setting

<sup>★</sup> Manufacture setting

<sup>★</sup> Manufacture setting

<sup>★</sup> Manufacture setting