

## XBA mini-LKR4

### XBA mini Option Switch Settings: 1(Pulse/MDB/JPSTD/ICT104U/ICT104V)

Supported bill LKR 20, 50, 100, 500 4bills.

XBA mini dip-switch settings and functions:

| FUNCTION                                           | SW1 | SW2                 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 | SW1 | SW2 | SW3 | SW4 |
|----------------------------------------------------|-----|---------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| ★ Reject LKR 20                                    | ON  |                     |     |     |     |     |     |     |     |     |     |     |
| ★ Accept LKR 20                                    | OFF |                     |     |     |     |     |     |     |     |     |     |     |
| ★ Reject LKR 50                                    |     | ON                  |     |     |     |     |     |     |     |     |     |     |
| ★ Accept LKR 50                                    |     | OFF                 |     |     |     |     |     |     |     |     |     |     |
| ★ Reject LKR 100                                   |     |                     | ON  |     |     |     |     |     |     |     |     |     |
| ★ Accept LKR 100                                   |     |                     | OFF |     |     |     |     |     |     |     |     |     |
| ★ Reject LKR 500                                   |     |                     |     | ON  |     |     |     |     |     |     |     |     |
| ★ Accept LKR 500                                   |     |                     |     | OFF |     |     |     |     |     |     |     |     |
| Reserved                                           |     |                     |     | ON  |     |     |     |     |     |     |     |     |
| ★ Reserved                                         |     |                     |     | OFF |     |     |     |     |     |     |     |     |
| ★ Disable Bill Reject 4 Times<br>BA Stop By 30 Sec |     |                     |     |     | ON  |     |     |     |     |     |     |     |
| ★ Enable Bill Reject 4 Times<br>BA Stop By 30 Sec  |     |                     |     |     | OFF |     |     |     |     |     |     |     |
| ★ Stack Banknote when Power-up                     |     |                     |     |     |     | ON  |     |     |     |     |     |     |
| ★ Reject Banknote when Power-up                    |     |                     |     |     |     | OFF |     |     |     |     |     |     |
| ★ Reserved                                         |     |                     |     |     |     |     | ON  |     |     |     |     |     |
| ★ Reserved                                         |     |                     |     |     |     |     | OFF |     |     |     |     |     |
| ★ Inhibit Active High                              |     |                     |     |     |     |     |     | ON  |     |     |     |     |
| ★ Inhibit Active Low                               |     |                     |     |     |     |     |     | OFF |     |     |     |     |
| ★ Reserved                                         |     |                     |     |     |     |     |     |     | ON  |     |     |     |
| ★ Reserved                                         |     |                     |     |     |     |     |     |     | OFF |     |     |     |
| ★ Interface Timing Conversion                      |     | 50ms LO / 50ms HI   |     |     |     |     |     |     |     | OFF | OFF |     |
|                                                    |     | 60ms LO / 300ms HI  |     |     |     |     |     |     |     | ON  | OFF |     |
|                                                    |     | 30ms LO / 50ms HI   |     |     |     |     |     |     |     | OFF | ON  |     |
|                                                    |     | 150ms LO / 150ms HI |     |     |     |     |     |     |     | ON  | ON  |     |

★ Manufacture setting

After setting dip switch of the credit pulses, you should reset the bill acceptor again.

Note : (1) Calibration card is needed.

## XBA mini-LKR4(Pulse/MDB/JPSTD/ICT104V/ICT104U)

### Interface Settings: 2(Pulse)

|   | INTERFACE                | SW1 | SW2 | SW3 | SW4 |
|---|--------------------------|-----|-----|-----|-----|
| ★ | Credit-Pulse Normal HIGH | ON  |     |     |     |
|   | Credit-Pulse Normal LOW  | OFF |     |     |     |
| ★ | Pulse Mode               |     | ON  |     |     |
|   | Other Mode               |     | OFF |     |     |
| ★ | 1 pulse / LKR 10         |     |     | OFF | OFF |
|   | 2 pulses / LKR 10        |     |     | OFF | ON  |
|   | 4 pulses / LKR 10        |     |     | ON  | OFF |
|   | 10 pulses / LKR 10       |     |     | ON  | ON  |

★ Manufacture setting

### Interface Settings: 2(MDB)

|   | FUNCTION                         | SW1 | SW2 | SW3 | SW4 |
|---|----------------------------------|-----|-----|-----|-----|
| ★ | Scaling Factor (SF) = 100        | ON  |     |     |     |
|   | Decimal Point Position (DPP) = 2 |     |     |     |     |
|   | Scaling Factor (SF) = 1          | OFF |     |     |     |
|   | Decimal Point Position (DPP) = 0 |     |     |     |     |
|   | Pulse Mode                       |     | ON  |     |     |
| ★ | Other Mode                       |     | OFF |     |     |
| ★ | MDB Mode                         |     |     | OFF | OFF |
|   | JPSTD Mode                       |     |     | OFF | ON  |
|   | ICT104U Mode                     |     |     | ON  | OFF |
|   | ICT104V Mode                     |     |     | ON  | ON  |

★ Manufacture setting

### Interface Settings: 2(ICT)

|   | FUNCTION     | SW1 | SW2 | SW3 | SW4 |
|---|--------------|-----|-----|-----|-----|
|   | Reserved     | ON  |     |     |     |
| ★ | Reserved     | OFF |     |     |     |
|   | Pulse Mode   |     | ON  |     |     |
| ★ | Other Mode   |     | OFF |     |     |
|   | MDB Mode     |     |     | OFF | OFF |
|   | JPSTD Mode   |     |     | OFF | ON  |
|   | ICT104U Mode |     |     | ON  | OFF |
|   | ICT104V Mode |     |     | ON  | ON  |

★ Manufacture setting

### Interface Settings: 2(ICT104V)

|   | FUNCTION                               | SW1 | SW2 | SW3 | SW4 |
|---|----------------------------------------|-----|-----|-----|-----|
| ★ | Connect with coin changer              | ON  |     |     |     |
|   | Connect with ICT JPSTD Converter board | OFF |     |     |     |
|   | Pulse Mode                             |     | ON  |     |     |
| ★ | Other Mode                             |     | OFF |     |     |
|   | MDB Mode                               |     |     | OFF | OFF |
|   | JPSTD Mode                             |     |     | OFF | ON  |
|   | ICT104U Mode                           |     |     | ON  | OFF |
|   | ICT104V Mode                           |     |     | ON  | ON  |

★ Manufacture setting

## Currency Assign Data

| Interface<br>Bill value | JPSTD  | ICT104U | ICT104V | Pulse   | MDB     |
|-------------------------|--------|---------|---------|---------|---------|
| BV1                     | LKR 20 | LKR 20  | LKR 20  | LKR 20  | LKR 20  |
| BV2                     |        | LKR 50  | LKR 50  | LKR 50  | LKR 50  |
| BV3                     |        | LKR 100 | LKR 100 | LKR 100 | LKR 100 |
| BV4                     |        | LKR 500 | LKR 500 | LKR 500 | LKR 500 |