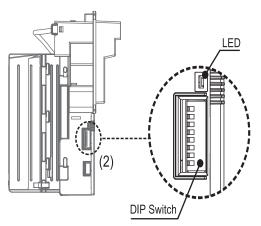
# **XBA-GEL6 Option Switch Settings**

Supported bill Lari 5, 10, 20, 50, 100, 200 6bills.

	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
	Reject Lari 5 & 10	ON							
*	Accept Lari 5 & 10	OFF							
	Reject Lari 20		ON						
*	Accept Lari 20		OFF						
	Reject Lari 50			ON					
*	Accept Lari 50			OFF					
	Reject Lari 100				ON				
*	Accept Lari 100				OFF				
	Reject Lari 200					ON			
*	Accept Lari 200					OFF			
	Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
*	Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
	Stack Banknote when Power-up							ON	
*	Reject Banknote when Power-up							OFF	
	Reserved						ON		
*	Reserved								OFF

This dip switch is located at the side of XBA.



### **Currency Assign Data**

nterface Bill value	Pulse	JPSTD	ccTalk	ICT104U	RS232 A0
BV1	Lari 5	Lari 5	Lari 5	Lari 5	Lari 5
BV2	Lari 10		Lari 10	Lari 10	Lari 10
BV3	Lari 20		Lari 20	Lari 20	Lari 20
BV4	Lari 50		Lari 50	Lari 50	Lari 50
BV5	Lari 100		Lari 100	Lari 100	Lari 100
BV6	Lari 200		Lari 200	Lari 200	Lari 200

#### RS232 A0 INFORMATION

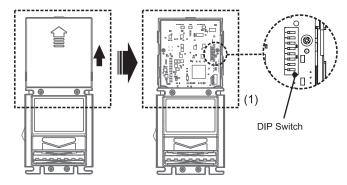
BillType Data						
Code	Denomination					
1	Reserved					
2	Reserved					
3	Lari 5					
4	Lari 10					
5	Lari 20					
6	Lari 50					
7	Lari 100					
8	Lari 200					
9	Reserved					

Appendix

## XBA-GEL6 (Pulse/JPSTD/ICT104U/RS232 A0/ccTalk)

	FUNCTION		SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
	Pulse Mode	Other Mode								
	Inhibit Active HIGH		ON							
*	Inhibit Active LOW		OFF							
	Reserved			ON						
*	Reserved			OFF						
*	50ms LO / 50ms HI		C		OFF	OFF				
	50ms LO / 100ms HI				OFF	ON				
	65ms LO / 75ms HI				ON	OFF				
	100ms LO / 150ms HI				ON	ON				
*	Credit-Pulse Normal HIGH	ccTalk 16-bit Encryption mode & Restoe Mode.(See Table.1)					ON			
	Credit-Pulse Normal LOW	ccTalk 16-bit Decrypt mode & 8-bit checksum(See Table.1)					OFF			
*	Pulse Mode							ON		
	Other Mode							OFF		
*	1 pulse / Lari 5	ccTalk Mode							OFF	OFF
	2 pulses / Lari 5	s / Lari 5 RS232 A0 Mode				OFF	ON			
	5 pulses / Lari 5	ICT104U Mode					ON	OFF		
	20 pulses / Lari 5 JPSTD Mode				ON	ON				

This dip switch is located on the CPU board, remove the CPU board cover first.



#### ★ Manufacture setting

Affer setting dip switch of the credit pulses, you should reset the bill acceptor again. Note: 1.Turn this dip to with restart the bill acceptor will restore.

- 2.ccTalk address to its default value of 40 and the encryption key to its default value of 123456.
- 3.Calibration card is needed.

Table 1

SW2 DIP 1	OFF	OFF	ON	★ ON
SW2 DIP 5	W2 DIP 5 OFF ON		OFF	<b>★</b> ON
	8-Bit Checksum	Restore mode (Note.2)	CRC 16-Bit Decrypt Checksum	CRC 16-Bit Encryption Checksum

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