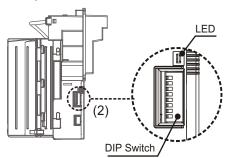
## **XBA-UYU7 Option Switch Settings**

Supported bill UYU 20, 50, 100, 200, 500, 1000, 2000 7bills.

|   | FUNCTION   | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 |
|---|--|-----|-----|-----|-----|-----|-----|-----|-----|
|   | Reject UYU 20 (Note.5)                           | ON  |     |     |     |     |     |     |     |
| * | Accept UYU 20 (Note.5)                           | OFF |     |     |     |     |     |     |     |
|   | Reject UYU 50                                    |     | ON  |     |     |     |     |     |     |
| * | Accept UYU 50                                    |     | OFF |     |     |     |     |     |     |
|   | Reject UYU 100                                   |     |     | ON  |     |     |     |     |     |
| * | Accept UYU 100                                   |     |     | OFF |     |     |     |     |     |
|   | Reject UYU 200 & 500                             |     |     |     | ON  |     |     |     |     |
| * | Accept UYU 200 & 500                             |     |     |     | OFF |     |     |     |     |
|   | Reject UYU 1000 & 2000                           |     |     |     |     | ON  |     |     |     |
| * | Accept UYU 1000 & 2000                           |     |     |     |     | OFF |     |     |     |
|   | Disable Bill Reject 4 Times<br>BA Stop By 30 Sec |     |     |     |     |     | ON  |     |     |
| * | Enable Bill Reject 4 Times<br>BA Stop By 30 Sec  |     |     |     |     |     | OFF |     |     |
|   | Stack Banknote when Power-up                     |     |     |     |     |     |     | ON  |     |
| * | Reject Banknote when Power-up                    |     |     |     |     |     |     | OFF |     |
|   | Reserved   |     |     |     |     |     |     |     | ON  |
| * | Reserved   |     |     |     |     |     |     |     | OFF |



## This dip switch is located at the side of XBA. **Currency Assign Data**

| Interface<br>Bill value | ICT002U |      | ICT104U |      | ICT104V |      | Pu<br>When Dip<br>1 is OFF |      | When Dip |      | MDB |      |
|-------------------------|---------|------|---------|------|---------|------|----------------------------|------|----------|------|-----|------|
| BV1                     | UYU     | 20   | UYU     | 20   | UYU     | 20   | UYU                        | 20   | 110      | 0.1  | UYU | 20   |
| BV2                     | UYU     | 50   | UYU     | 50   | UYU     | 50   | UYU                        | 50   | UYU      | 50   | UYU | 50   |
| BV3                     | UYU     | 100  | UYU     | 100  | UYU     | 100  | UYU                        | 100  | UYU      | 100  | UYU | 100  |
| BV4                     | UYU     | 200  | UYU     | 200  | UYU     | 200  | UYU                        | 200  | UYU      | 200  | UYU | 200  |
| BV5                     | UYU     | 500  | UYU     | 500  | UYU     | 500  | UYU                        | 500  | UYU      | 500  | UYU | 500  |
| BV6                     | UYU     | 1000 | UYU     | 1000 | UYU     | 1000 | UYU                        | 1000 | UYU      | 1000 | UYU | 1000 |
| BV7                     | UYU     | 2000 | UYU     | 2000 | UYU     | 2000 | UYU:                       | 2000 | UYU      | 2000 | UYU | 2000 |

## XBA-UYU7 (Pulse/MDB/ICT002U/ICT104U/ICT104V)

|   | FUNCTION                    | SW1  | SW2 | SW3 | SW4     | SW5                                   |     | SW6 | SW7 | SW8 |
|---|-----------------------------|--|-----|-----|---------|---------------------------------------|-----|-----|-----|-----|
|   | Inhibit Active HIGH         | ON   |     |     |         |                                       |     |     |     |     |
| * | Inhibit Active LOW          | OFF  |     |     |         |                                       |     |     |     |     |
|   | Reserved                    |  | ON  |     |         |                                       |     |     |     |     |
| * | Reserved                    |  | OFF |     |         |                                       |     |     |     |     |
|   | Reserved                    |  |     | ON  |         |                                       |     |     |     |     |
| * | Reserved                    |  |     | OFF |         |                                       |     |     |     |     |
|   | Reserved                    |  |     |     | ON      |                                       |     |     |     |     |
| * | Reserved                    |  |     |     | OFF     |                                       |     |     |     |     |
|   | Pulse Mode                  |  | MDB |     |         | ICT104V Mode                          |     |     |     |     |
| * | Credit-Pulse Normal HIGH    | Scaling Factor (SF) = 10<br>Decimal Point Position (DPF  |     |     | PP) = 2 | Connect with coin changer             | ON  |     |     |     |
|   | Credit-Pulse Normal LOW     | Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0 |     |     |         | Connect with ICT VCCS converter board | OFF |     |     |     |
| * | Pulse Mode                  | C  |     |     |         |                                       |     |     |     |     |
|   | Other Mode                  | OFF  |     |     |         |                                       |     |     |     |     |
|   | Pulse Mode                  | Other Mode   |     |     |         |                                       |     |     |     |     |
| * | 1 pulse / UYU 10 (Note.5)   | MDB Mode   |     |     |         |                                       |     |     |     | OFF |
|   | 2 pulses / UYU 10 (Note.5)  | ICT002U Mode   |     |     |         |                                       |     |     |     | ON  |
|   | 5 pulses / UYU 10 (Note.5)  | ICT104U Mode   |     |     |         |                                       |     |     |     | OFF |
|   | 10 pulses / UYU 10 (Note.5) | ICT104V Mode   |     |     |         |                                       |     |     |     | ON  |

## ★ Manufacture setting

Note: 1. After setting dip switch of the credit pulses, you should reset the bill acceptor again.

- 2. Please reset the bill acceptor after set the dip switch.
- 3. Dip switches 1 to 5 are only used for pulse protocol.
- 4. Calibration card is needed.
- 5. Pulse Mode: Dip 1 turn OFF,One Pulse per UYU 10. Dip 1 turn ON ,One Pulse per UYU 50.

This dip switch is located on the CPU board, remove the CPU board cover first.

