TAO-A.V-PKR6

TAO-A.V Option Switch Settings: 1(Pulse/ICT104V/ICT104U/MDB)

Supported bill PRs 10, 20, 50, 100, 500, 1000 6bills.

TAO-A.V dip-switch settings and functions:

1		_											
	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW1	SW2	SW3	SW4
	Reject PRs 10 & 20	ON											
*	Accept PRs 10 & 20	OFF											
	Reject PRs 50		ON										
*	Accept PRs 50		OFF										
	Reject PRs 100			ON									
*	Accept PRs 100			OFF									
	Reject PRs 500				ON								
*	Accept PRs 500				OFF								
	Reject PRs 1000					ON							
*	Accept PRs 1000					OFF							
	Reserved						ON						
*	Reserved						OFF						
	Pulse Mode	Other Mode											
	Harness Disable	BA stops working when fishing ON											
*	Harness Enable	Normal OFF											
	Inhibit Active High	ON			ON								
*	Inhibit Active Low								OFF				
*	1 pulse / PRs10	OFF OFF											
	5 pulses / PRs10	ON OFF											
	10 pulses / PRs10									OFF	ON		
	20 pulses / PRs10									ON	ON		
*		50r	ns LO	/ 50n	ns HI						1	OFF	OFF
		60r	60ms LO / 300ms HI								ON	OFF	
	Interface Timing Conversion	30ms LO / 50ms HI					OFF	ON					
		150r	ns LO	/ 150n	ns HI							ON	ON

★ Manufacture setting

Note : (1) Default setting.

(2) Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.
(3) Remove the front cover and the dip-switch in on the CPU board. (Please refer to "SW1" setting.)

Appendix

TAO-A.V-PKR6(Pulse/ICT104V/ICT104U/MDB)

Interface Settings: 3(Pulse)

Currency Assign Data

INTERFACE	SW1	SW2	SW3	SW4
Credit-Pulse Normal HIGH	ON			
Credit-Pulse Normal LOW	OFF			
Pulse Mode		ON	OFF	
MDB Interface		ON	ON	
ICT104V Interface		OFF	ON	
ICT104U Interface		OFF	OFF	
Decerved				ON
Reserved				
	Credit-Pulse Normal HIGH Credit-Pulse Normal LOW Pulse Mode MDB Interface ICT104V Interface	Credit-Pulse Normal HIGH ON Credit-Pulse Normal LOW OFF Pulse Mode OFF MDB Interface ICT104V Interface ICT104U Interface ICT104U Interface	Credit-Pulse Normal HIGH ON Credit-Pulse Normal LOW OFF Pulse Mode ON MDB Interface ON ICT104V Interface OFF ICT104U Interface OFF	Credit-Pulse Normal HIGH ON Credit-Pulse Normal LOW OFF Pulse Mode ON OFF MDB Interface ON ON ICT104V Interface OFF ON ICT104U Interface OFF OFF

Interface ICT104U Pulse ICT104V MDB PRs 10 BV1 PRs 10 PRs 10 PRs 10 PRs 20 PRs 20 PRs 20 BV2 PRs 20 BV3 PRs 50 PRs 50 PRs 50 PRs 50 BV4 PRs 100 PRs 100 PRs 100 PRs 100 BV5 PRs 500 PRs 500 PRs 500 PRs 500 PRs 1000 PRs 1000 BV6 PRs 1000 PRs 1000

★ Manufacture setting

Interface Settings: 3(ICT104V)

	INTERFACE	SW1	SW2	SW3	SW4
*	Reserved	ON			
	Reserved	OFF			
	Pulse Mode		ON	OFF	
*	MDB Interface		ON	ON	
	ICT104V Interface		OFF	ON	
	ICT104U Interface		OFF	OFF	
*	Reserved				ON
	Reserved				OFF

Interface Settings: 3(ICT104U)

	INTERFACE	SW1	SW2	SW3	SW4	
★	Reserved	ON				
	Reserved	OFF				
	Pulse Mode		ON	OFF		
	MDB Interface		ON	ON		
	ICT104V Interface		OFF	ON		
*	ICT104U Interface		OFF	OFF		
	Reserved				ON	
*	Reserveu		OFF			

★ Manufacture setting

★ Manufacture setting

Interface Settings: 3(MDB)

	FUNCTION	SW1	SW2	SW3	SW4
*	Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2				
	Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0				
*	Pulse Mode		ON	OFF	
	MDB Interface		ON	ON	
	ICT104V Interface		OFF	ON	
	ICT104U Interface		OFF	OFF	
*	Reserved				ON
	iveseived				OFF

★ Manufacture setting

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