TAO-A.V-SAR2

TAO-A.V Option Switch Settings: 1(Pulse/ICT104V/MDB/ICT104U) Supported bill SAR 1, 5 2bills.

TAO-A.V dip-switch settings and functions:

	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW1	SW2	SW3	SW4
	Reject SRis 1	ON											
*	Accept SRis 1	OFF											
	Reject SRis 5		ON										
*	Accept SRis 5		OFF										
	Reserved			ON									
*	Reserved			OFF									
	Reserved				ON								
*	Reserved				OFF								
	Reserved					ON							
*	Reserved					OFF							
	High Acceptance (Note.3)						ON						
*	High Security						OFF						
	Pulse Mode	Other Mode											
	Harness Disable	BA	stops	worki	ng whe	en fishi	ing	ON					
*	Harness Enable			Nor	mal			OFF					
	Inhibit Active High								ON				
*	Inhibit Active Low								OFF				
*	1 pulse / SRis 1									OFF	OFF		
	2 pulses / SRis 1									ON	OFF		
	4 pulses / SRis 1									OFF	ON		
	20 pulses / SRis 1									ON	ON		
*		50r	ns LO	/ 50n	ns HI						•	OFF	OFF
	Interface Timing Conversion	60r	ns LO	/ 300r	ns HI							ON	OFF
	interiace filling Conversion	30r	ns LO	/ 50n	ns HI							OFF	ON
		150r	ns LO	/ 150n	ns HI							ON	ON

- ★ Manufacture setting
- Note: (1) Default setting
 - (2) Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.
 - (3) High acceptance mode will increasing accepting rate, however, it will reduce the security level of Bill Acceptor.

Appendix

TAO-A.V-SAR2(Pulse/ICT104V/MDB/ICT104U)

Interface Settings: 3(Pulse)

INTERFACE	SW1	SW2	SW3	SW4
Credit-Pulse Normal HIGH	ON			
Credit-Pulse Normal LOW	OFF			
Pulse Mode		ON	OFF	
MDB Mode		ON	ON	
ICT104V Mode		OFF	ON	
ICT104U Mode		OFF	OFF	
Deserved				ON
Reserved				OEE

Currency Assign Data

Interface Bill value	ICT104U	Pulse	MDB	ICT104V	
BV1	SRis 1	SRis 1	SRis 1	SRis 1	
BV2	SRis 5	SRis 5	SRis 5	SRis 5	

Interface Settings: 3(ICT104V)

Interface Settings: 3(ICT104U)

	INTERFACE	SW1	SW2	SW3	SW4
*	Reserved	ON			
	Reserved	OFF			
	Pulse Mode		ON	OFF	
	MDB Mode		ON	ON	
*	ICT104V Mode		OFF	ON	
	ICT104U Mode		OFF	OFF	
	Reserved				ON
*	Neserveu				OFF

ı	INTERFACE	SW1	SW2	SW3	SW4
-	Reserved	ON			
	Reserved	OFF			
	Pulse Mode		ON	OFF	
	MDB Mode		ON	ON	
	ICT104V Mode		OFF	ON	
-	ICT104U Mode		OFF	OFF	
	Reserved				ON
-	Reserved				OFF
					OFF

Interface Settings: 3(MDB)

	FUNCTION	SW1	SW2	SW3	SW4
*	Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2	ON			
	Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0	OFF			
	Pulse Mode		ON	OFF	
*	MDB Mode		ON	ON	
	ICT104V Mode		OFF	ON	
	ICT104U Mode		OFF	OFF	
	Reserved				ON
*	i veselveu				

[★] Manufacture setting

[★] Manufacture setting

[★] Manufacture setting

[★] Manufacture setting