TAO-V-SAR2

TAO-V Option Switch Settings: 1(Pulse/ICT/MDB/ICT for VCCS)

Supported bill SRis 1, 5 2bills.

TAO-V dip-switch settings and functions:

1													
	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW1	SW2	SW3	SW4
	Reject SRis 1	ON											
*	Accept SRis 1	OFF											
	Reject SRis 5		ON										
*	Accept SRis 5		OFF										
	Reserved			ON									
*	Reserved			OFF									
	Reserved				ON								
*	Reserved				OFF								
	Reserved					ON							
*	Reserved					OFF							
	Reserved						ON						
*	Reserved						OFF						
	Harness Disable							ON					
*	Harness Enable							OFF					
	Inhibit Active High						ON						
*	Inhibit Active Low	OFF											
*	1 pulse / SRis 1	OFF OFF											
	2 pulse / SRis 1									ON	OFF		
	4 pulse / SRis 1									OFF	ON		
	20 pulse / SRis 1									ON	ON		
*		50r	ns on <i>i</i>	50m	s off							OFF	OFF
	Interface Timing Conversion	60ms on / 300ms off									ON	OFF	
	menace mining conversion	30ms on / 50ms off								OFF	ON		
		150ms on / 150ms off								ON	ON		

 \star Manufacture setting

Note : (1) Please reset the bill acceptor after any changes on Dip switch.

(2) SW7 and SW8 of the 8-switch DIP and the 4-switch DIP are for pulse protocol only.

TAO-V-SAR2

Interface Settings: 2(Pulse)

INTERFACE dit-Pulse Normal HIGH dit-Pulse Normal LOW	SW1 ON	SW2	SW3	SW4		
	-					
dit-Pulse Normal LOW						
	OFF					
Pulse Mode		ON	OFF			
ICT Mode		OFF	OFF			
ICT for VCCS		OFF	ON			
MDB Interface		ON	ON			
Reserved						
	Pulse Mode ICT Mode ICT for VCCS MDB Interface	Pulse Mode ICT Mode ICT for VCCS MDB Interface	Pulse Mode ON ICT Mode OFF ICT for VCCS OFF MDB Interface ON	Pulse Mode ON OFF ICT Mode OFF OFF ICT for VCCS OFF ON MDB Interface ON ON		

★ Manufacture setting

Interface Settings: 2(ICT/ICT for VCCS)



 \star Manufacture setting

★ Manufacture setting

Interface Settings: 2(MDB)

	FUNCTION	SW1	SW2	SW3	SW4	
*	Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2	ON				
	Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0	OFF				
	Pulse Mode		ON	OFF		
	ICT Mode		OFF	OFF		
	ICT for VCCS		OFF	ON		
\star	MDB Interface		ON	ON		
	Reserved					
*						

★ Manufacture setting

Appendix