#### V7-AMD2

# V7 Option Switch Settings: 1 (Pulse/ICT004/MDB) Supported bill DRAMS 1000, 2000 2bills.

V7 dip-switch settings and functions:

|   | FUNCTION                 | SW1                | SW2     | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 | SW1 | SW2 | SW3 | SW4 |
|---|--------------------------|--------------------|---------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
|   | Reject DRAMS 1000(New)   | ON                 |         |     |     |     |     |     |     |     |     |     |     |
| * | Accept DRAMS 1000(New)   | OFF                |         |     |     |     |     |     |     |     |     |     |     |
|   | Reject DRAMS 2000        |                    | ON      |     |     |     |     |     |     |     |     |     |     |
| * | Accept DRAMS 2000        |                    | OFF     |     |     |     |     |     |     |     |     |     |     |
|   | Reject DRAMS 1000(Old)   |                    |         | ON  |     |     |     |     |     |     |     |     |     |
| * | Accept DRAMS 1000(Old)   |                    |         | OFF |     |     |     |     |     |     |     |     |     |
|   | Reserved                 |                    |         |     | ON  |     |     |     |     |     |     |     |     |
| * | Reserved                 |                    |         |     | OFF |     |     |     |     |     |     |     |     |
|   | Reserved                 |                    |         |     |     | ON  |     |     |     |     |     |     |     |
| * | Reserved                 |                    |         |     |     | OFF |     |     |     |     |     |     |     |
|   | Push Out Stacker Enable  |                    |         |     |     |     | ON  |     |     |     |     |     |     |
| * | Push Out Stacker Disable |                    |         |     |     |     | OFF |     |     |     |     |     |     |
|   | Harness Disable          |                    |         |     |     |     |     | ON  |     |     |     |     |     |
| * | Harness Enable           |                    |         |     |     |     |     | OFF |     |     |     |     |     |
|   | Inhibit Active High      |                    | ON      |     |     |     |     |     |     |     |     |     |     |
| * | Inhibit Active Low       | OFF                |         |     |     |     |     |     |     |     |     |     |     |
| * | 1 pulse / DRAMS 1000     |                    | OFF OFF |     |     |     |     |     |     |     |     |     |     |
|   | 2 pulses / DRAMS 1000    |                    |         |     |     |     |     |     |     | ON  | OFF |     |     |
|   | 4 pulses / DRAMS 1000    |                    |         |     |     |     |     |     |     | OFF | ON  |     |     |
|   | 20 pulses / DRAMS 1000   |                    |         |     |     |     |     |     |     | ON  | ON  |     |     |
| * |                          | 50ms LO / 50ms HI  |         |     |     |     |     | OFF | OFF |     |     |     |     |
|   | Pulse Speed              | 60ms LO / 300ms HI |         |     |     |     |     |     | ON  | OFF |     |     |     |
|   | 1 4100 00004             | 30ms LO / 50ms HI  |         |     |     |     |     |     | OFF | ON  |     |     |     |
|   | 150ms LO / 150ms HI      |                    |         |     |     | ON  | ON  |     |     |     |     |     |     |

Note: (1) Default setting.

- (2) Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.
- (3) Remove the front cover and the dip-switch in on the CPU board.

(Please refer to "SW1" setting.)

## V7-AMD2(Pulse/ICT004/MDB)

#### **V7 Interface Settings: 3 (ICT004)**

|   | INTERFACE        | SW1 | SW2 | SW3 | SW4 |
|---|------------------|-----|-----|-----|-----|
|   | Reserved         | ON  |     |     |     |
| * | Reserved         | OFF |     |     |     |
|   | Pulse Mode       |     | ON  | OFF |     |
|   | MDB Interface    |     | OFF | OFF |     |
| * | ICT004 Interface |     | OFF | ON  |     |
|   | Reserved         |     |     |     | ON  |
| * | Reserved         |     |     |     | OFF |

<sup>★</sup> Manufacture setting

### **V7 Interface Settings: 3 (Pulse)**

|   | INTERFACE                | SW1 | SW2 | SW3    | SW4 |  |
|---|--------------------------|-----|-----|--------|-----|--|
| * | Credit-Pulse Normal High | ON  |     |        |     |  |
|   | Credit-Pulse Normal Low  | OFF |     |        |     |  |
| * | Pulse Mode               |     | ON  | ON OFF |     |  |
|   | MDB Interface OFF OFF    |     | OFF |        |     |  |
|   | ICT004 Interface         |     | OFF | ON     |     |  |
|   | Reserved                 |     |     |        | ON  |  |
| * | Reserved                 |     | OFF |        |     |  |

<sup>★</sup> Manufacture setting

## **V7 Interface Settings: 3 (MDB)**

|   | INTERFACE   | SW1 | SW2 | SW3 | SW4 |
|---|---|-----|-----|-----|-----|
| * | Scaling Factor (SF) = 100<br>Decimal Point Position (DPP) = 2 | ON  |     |     |     |
|   | Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0      | OFF |     |     |     |
|   | Pulse Mode  |     | ON  | OFF |     |
| * | MDB Interface   |     | OFF | OFF |     |
|   | ICT004 Interface  |     | OFF | ON  |     |
|   | Credit divided by 10  |     |     |     | ON  |
| * | Reserved  |     |     |     | OFF |

<sup>★</sup> Manufacture setting

# **Currency Assign Data**

| Interface<br>Bill value | ICT004          | Pulse           | MDB             |
|-------------------------|-----------------|-----------------|-----------------|
| BV1                     | DRAMS 1000(New) | DRAMS 1000(New) | DRAMS 1000(New) |
| BV2                     | DRAMS 2000      | DRAMS 2000      | DRAMS 2000      |
| BV3                     | DRAMS 1000(Old) | DRAMS 1000(Old) | DRAMS 1000(Old) |