

XBA-IQD3 Option Switch Settings

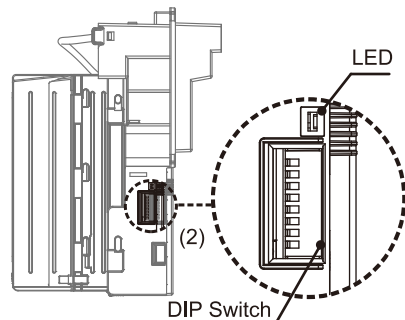
Supported bill IQD 250, 500, 1000 3bills.

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
★ Reject IQD 250	ON							
★ Accept IQD 250	OFF							
★ Reject IQD 500		ON						
★ Accept IQD 500		OFF						
★ Reject IQD 1000			ON					
★ Accept IQD 1000			OFF					
★ Reserved				ON				
★ Reserved				OFF				
★ Reserved					ON			
★ Reserved					OFF			
★ Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
★ Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
★ Stack Banknote when Power-up							ON	
★ Reject Banknote when Power-up							OFF	
★ Reserved								ON
★ Reserved								OFF

★ Manufacture setting

Note: After setting dip switch of the credit pulses, you should reset the bill acceptor again.

This dip switch is located at the side of XBA.



Currency Assign Data

Interface	ICT104U	ICT104V	Pulse	MDB
BV1	IQD 250	IQD 250	IQD 250	IQD 250
BV2	IQD 500	IQD 500	IQD 500	IQD 500
BV3	IQD 1000	IQD 1000	IQD 1000	IQD 1000

XBA-IQD3 (Pulse/MDB/ICT104U/ICT104V)

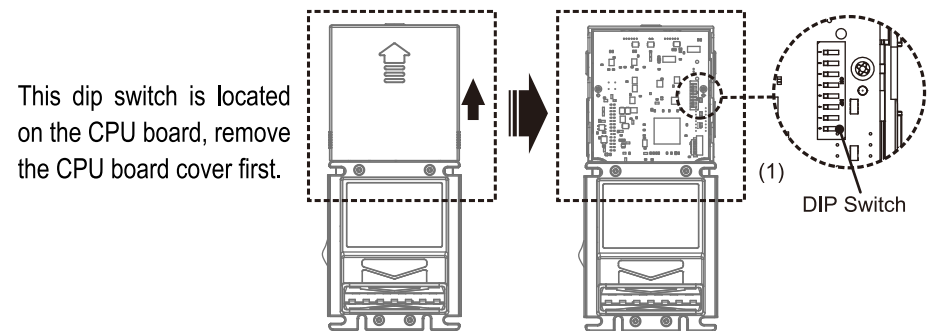
FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
★ Inhibit Active HIGH	ON							
★ Inhibit Active LOW	OFF							
★ Reserved		ON						
★ Reserved		OFF						
★ Reserved			OFF					
★ Reserved			OFF					
★ Reserved				OFF				
★ Reserved				ON				
Pulse Mode	MDB Mode		ICT104V Mode					
★ Credit-Pulse Normal HIGH	Scaling Factor (SF)= 1000 Decimal Point Position (DPP)= 3		Connect with coin changer			ON		
★ Credit-Pulse Normal LOW	Scaling Factor (SF)= 1 Decimal Point Position (DPP)= 0		Connect with ICT JPSTD converter board			OFF		
★ Pulse Mode						ON		
★ Other Mode						OFF		
Pulse Mode	Other Mode							
★ 1 pulse / IQD 250	MDB Mode						OFF	OFF
★ 5 pulses / IQD 250	Reserved						OFF	ON
★ 10 pulses / IQD 250	ICT104U Mode						ON	OFF
★ 25 pulses / IQD 250	ICT104V Mode						ON	ON

★ Manufacture setting

Note: 1. Please reset the bill acceptor after set the dip switch.

2. Dip switches 1 to 5 are only used for pulse protocol.

3. Calibration card is needed.



This dip switch is located on the CPU board, remove the CPU board cover first.