

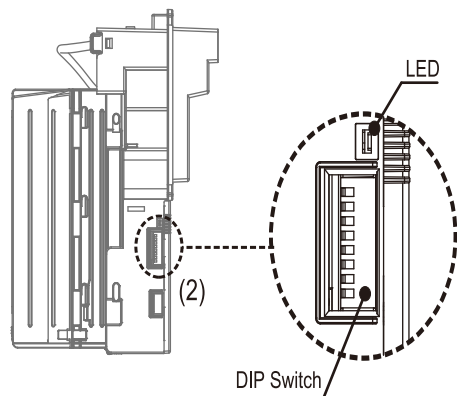
XBA-QAR3 Option Switch Settings

Supported bill QAR 1, 5, 10 3bills.

| FUNCTION | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 |
|--|-----|-----|-----|-----|-----|-----|-----|-----|
| ★ Reject QAR 1 (Note.4) | ON | | | | | | | |
| ★ Accept QAR 1 (Note.4) | OFF | | | | | | | |
| ★ Reject QAR 5 | | ON | | | | | | |
| ★ Accept QAR 5 | | OFF | | | | | | |
| ★ Reject QAR 10 | | | ON | | | | | |
| ★ Accept QAR 10 | | | OFF | | | | | |
| ★ Reserved | | | | ON | | | | |
| ★ Reserved | | | | OFF | | | | |
| ★ Reserved | | | | | ON | | | |
| ★ Reserved | | | | | OFF | | | |
| ★ Disable Bill Reject 4 Times BA Stop By 30 Sec | | | | | | ON | | |
| ★ Enable Bill Reject 4 Times BA Stop By 30 Sec | | | | | | OFF | | |
| ★ Stack Banknote when Power-up | | | | | | | ON | |
| ★ Reject Banknote when Power-up | | | | | | | OFF | |
| ★ Reserved | | | | | | | | ON |
| ★ Reserved | | | | | | | | OFF |

This dip switch is located at the side of XBA.

Currency Assign Data

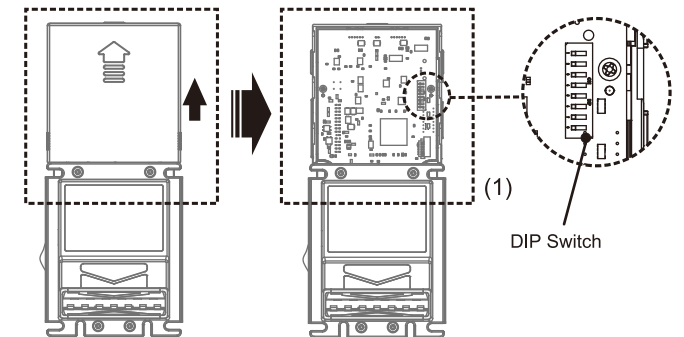


| Interface | ICT104U | | ICT104V | | Pulse | | MDB |
|-----------|------------|--------|----------|-----------|--------|--------|-----|
| | Bill value | | Dip 1 ON | Dip 1 OFF | | | |
| BV1 | QAR 1 | QAR 1 | | | QAR 1 | QAR 1 | |
| BV2 | QAR 5 | QAR 5 | QAR 5 | QAR 5 | QAR 5 | QAR 5 | |
| BV3 | QAR 10 | QAR 10 | QAR 10 | QAR 10 | QAR 10 | QAR 10 | |

XBA-QAR3 (Pulse/MDB/ICT104U/ICT104V)

| FUNCTION | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 |
|----------------------------|---|-------------------|---|---------------------|-----|-----|-----|-----|
| Inhibit Active HIGH | ON | | | | | | | |
| ★ Inhibit Active LOW | OFF | | | | | | | |
| ★ Reserved | | ON | | | | | | |
| ★ Reserved | | OFF | | | | | | |
| ★ Reserved | | | ON | | | | | |
| ★ Reserved | | | OFF | | | | | |
| ★ Reserved | | | | ON | | | | |
| ★ Reserved | | | | OFF | | | | |
| Pulse Mode | | MDB Mode | | ICT104V Mode | | | | |
| ★ Credit-Pulse Normal HIGH | Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2 | | Connect with coin changer | | ON | | | |
| ★ Credit-Pulse Normal LOW | Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0 | | Connect with ICT JPSTD converter board | | OFF | | | |
| ★ Pulse Mode | | | | | | ON | | |
| ★ Other Mode | | | | | | OFF | | |
| Pulse Mode | | Other Mode | | | | | | |
| ★ 1 pulse / QAR 1 & 5 | MDB Mode | | | | | | OFF | OFF |
| ★ 2 pulses / QAR 1 & 5 | Reserved | | | | | | OFF | ON |
| ★ 4 pulses / QAR 1 & 5 | ICT104U Mode | | | | | | ON | OFF |
| ★ 20 pulses / QAR 1 & 5 | ICT104V Mode | | | | | | ON | ON |

This dip switch is located on the CPU board, remove the CPU board cover first.



★ Manufacture setting

After setting dip switch of the credit pulses, you should reset the bill acceptor again.

- Note :
1. Please reset the bill acceptor after set the dip switch.
 2. Dip switches 1 to 5 are only used for pulse protocol.
 3. Calibration card is needed.
 4. Pulse Mode : When Dip 1 is OFF, One Pulse per QAR 1
When Dip 1 is ON, One Pulse per QAR 5