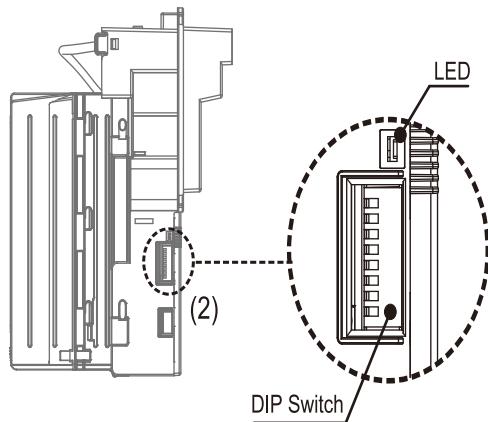


XBA-RSD8 Option Switch Settings

Supported bill RSD 10, 20, 50, 100, 200, 500, 1000, 2000 8bills.

| FUNCTION | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 |
|--|-----|-----|-----|-----|-----|-----|-----|-----|
| ★ Reject RSD 50 | ON | | | | | | | |
| ★ Accept RSD 50 | OFF | | | | | | | |
| ★ Reject RSD 100 | | ON | | | | | | |
| ★ Accept RSD 100 | | OFF | | | | | | |
| ★ Reject RSD 200 & 500 | | | ON | | | | | |
| ★ Accept RSD 200 & 500 | | | OFF | | | | | |
| ★ Reject RSD 1000 | | | | ON | | | | |
| ★ Accept RSD 1000 | | | | OFF | | | | |
| ★ Reject RSD 2000 | | | | | ON | | | |
| ★ Accept RSD 2000 | | | | | OFF | | | |
| ★ Disable Bill Reject 4 Times BA Stop By 30 Sec | | | | | | ON | | |
| ★ Enable Bill Reject 4 Times BA Stop By 30 Sec | | | | | | OFF | | |
| ★ Stack Banknote when Power-up | | | | | | | ON | |
| ★ Reject Banknote when Power-up | | | | | | | OFF | |
| ★ Reject RSD 10 & 20 | | | | | | | | ON |
| ★ Accept RSD 10 & 20 | | | | | | | | OFF |

This dip switch is located at the side of XBA.



Currency Assign Data

| Interface | Pulse | RS232 A0 | ICT104U | ccNet | MDB |
|-----------|----------|----------|----------|----------|----------|
| BV1 | RSD 10 | RSD 10 | RSD 10 | RSD 10 | RSD 10 |
| BV2 | RSD 20 | RSD 20 | RSD 20 | RSD 20 | RSD 20 |
| BV3 | RSD 50 | RSD 50 | RSD 50 | RSD 50 | RSD 50 |
| BV4 | RSD 100 | RSD 100 | RSD 100 | RSD 100 | RSD 100 |
| BV5 | RSD 200 | RSD 200 | RSD 200 | RSD 200 | RSD 200 |
| BV6 | RSD 500 | RSD 500 | RSD 500 | RSD 500 | RSD 500 |
| BV7 | RSD 1000 | RSD 1000 | RSD 1000 | RSD 1000 | RSD 1000 |
| BV8 | RSD 2000 | RSD 2000 | RSD 2000 | RSD 2000 | RSD 2000 |

RS232 A0 INFORMATION

| BillType Data | |
|---------------|--------------|
| Code | Denomination |
| 1 | RSD 10 |
| 2 | RSD 20 |
| 3 | RSD 50 |
| 4 | RSD 100 |
| 5 | RSD 200 |
| 6 | RSD 500 |
| 7 | RSD 1000 |
| 8 | Reserved |
| 9 | RSD 2000 |

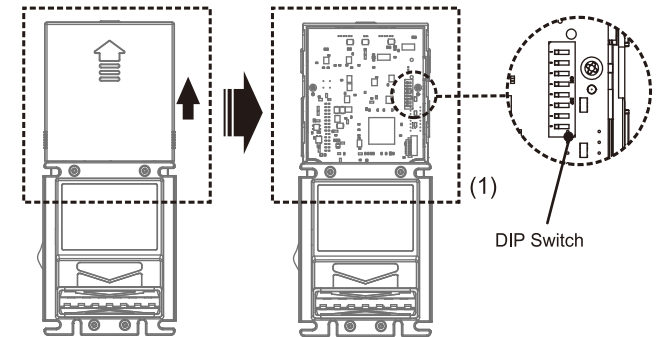
Appendix

XBA-RSD8 (Pulse/MDB/RS232 A0/ccNet/ICT104U)

| FUNCTION | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 | |
|----------------------------|--|-----|-------------------|-----|-----|-----|-----|-----|-----|
| Inhibit Active HIGH | ON | | | | | | | | |
| ★ Inhibit Active LOW | OFF | | | | | | | | |
| ★ Reserved | | ON | | | | | | | |
| ★ Reserved | | OFF | | | | | | | |
| ★ Reserved | | | ON | | | | | | |
| ★ Reserved | | | OFF | | | | | | |
| ★ Reserved | | | | ON | | | | | |
| ★ Reserved | | | | OFF | | | | | |
| Pulse Mode | MDB Mode | | ccNet Mode | | | | | | |
| ★ Credit-Pulse Normal HIGH | Scaling Factor (SF) = 100 Decimal Point Position(DPP) = 2 | | 9600 Baud Rate | | | ON | | | |
| Credit-Pulse Normal LOW | Scaling Factor (SF) = 1 Decimal Point Position(DPP) = 0 | | 19200 Baud Rate | | | OFF | | | |
| ★ Pulse Mode | | | | | | ON | | | |
| Other Mode | | | | | | OFF | | | |
| ★ 1 pulse / RSD 10 | MDB Mode | | | | | | | OFF | OFF |
| 5 pulses / RSD 10 | RS232 A0 Mode | | | | | | | OFF | ON |
| 10 pulses / RSD 10 | ccNet Mode | | | | | | | ON | OFF |
| 20 pulses / RSD 10 | ICT104U Mode | | | | | | | ON | ON |

★ Manufacture setting

This dip switch is located on the CPU board, remove the CPU board cover first.



Note : (1) Please reset the bill acceptor after set the dip switch.
(2) Dip switches 1 to 5 are only used for pulse protocol.
(3) Calibration card is needed.