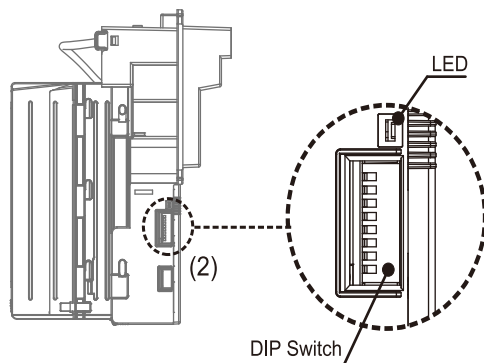


XBA-SAR4 Option Switch Settings

Supported bill SAR 1, 5, 10, 50 4bills.

| FUNCTION | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 |
|--|-----|-----|-----|-----|-----|-----|-----|-----|
| ★ Reject SAR 1 | ON | | | | | | | |
| ★ Accept SAR 1 | OFF | | | | | | | |
| ★ Reject SAR 5 | | ON | | | | | | |
| ★ Accept SAR 5 | | OFF | | | | | | |
| ★ Reject SAR 10 | | | ON | | | | | |
| ★ Accept SAR 10 | | | OFF | | | | | |
| ★ Reject SAR 50 | | | | ON | | | | |
| ★ Accept SAR 50 | | | | OFF | | | | |
| ★ High Acceptance <small>(Note.4)</small> | | | | | ON | | | |
| ★ High Security | | | | | OFF | | | |
| ★ Disable Bill Reject 4 Times BA Stop By 30 Sec | | | | | | ON | | |
| ★ Enable Bill Reject 4 Times BA Stop By 30 Sec | | | | | | OFF | | |
| ★ Stack Banknote when Power-up | | | | | | | ON | |
| ★ Reject Banknote when Power-up | | | | | | | OFF | |
| ★ Reserved | | | | | | | | ON |
| ★ Reserved | | | | | | | | OFF |

This dip switch is located at the side of XBA.



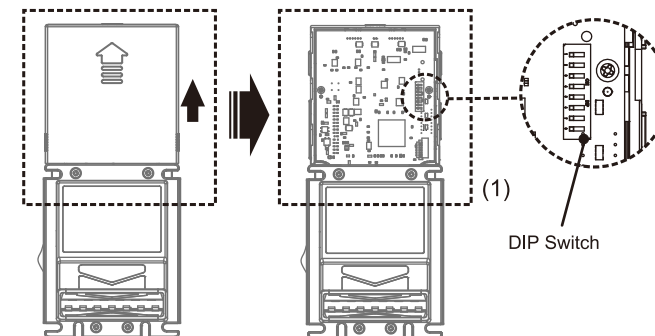
Currency Assign Data

| Interface | ICT104U | ICT104V | Pulse | MDB |
|-----------|---------|---------|--------|--------|
| BV1 | SAR 1 | SAR 1 | SAR 1 | SAR 1 |
| BV2 | SAR 5 | SAR 5 | SAR 5 | SAR 5 |
| BV3 | SAR 10 | SAR 10 | SAR 10 | SAR 10 |
| BV4 | | | | |
| BV5 | SAR 50 | SAR 50 | SAR 50 | SAR 50 |

XBA-SAR4 (Pulse/MDB/ICT104U/ICT104V)

| FUNCTION | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 |
|----------------------------|---|-----|---|-----|-----|-----|-----|-----|
| Inhibit Active HIGH | ON | | | | | | | |
| ★ Inhibit Active LOW | OFF | | | | | | | |
| ★ Reserved | | ON | | | | | | |
| ★ Reserved | | OFF | | | | | | |
| ★ Reserved | | | ON | | | | | |
| ★ Reserved | | | OFF | | | | | |
| ★ Reserved | | | | ON | | | | |
| ★ Reserved | | | | OFF | | | | |
| Pulse Mode | MDB Mode | | ICT104V Mode | | | | | |
| ★ Credit-Pulse Normal HIGH | Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2 | | Connect with coin changer | | ON | | | |
| ★ Credit-Pulse Normal LOW | Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0 | | Connect with ICT JPSTD converter board | | OFF | | | |
| ★ Pulse Mode | | | | | | ON | | |
| ★ Other Mode | | | | | | OFF | | |
| Pulse Mode | Other Mode | | | | | | | |
| ★ 1 pulse / SAR 1 | MDB Mode | | | | | | OFF | OFF |
| ★ 2 pulses / SAR 1 | Reserved | | | | | | OFF | ON |
| ★ 4 pulses / SAR 1 | ICT104U Mode | | | | | | ON | OFF |
| ★ 20 pulses / SAR 1 | ICT104V Mode | | | | | | ON | ON |

This dip switch is located on the CPU board, remove the CPU board cover first.



★ Manufacture setting

- Note :
- (1) Please reset the bill acceptor after set the dip switch.
 - (2) Dip switches 1 to 5 are only used for pulse protocol.
 - (3) Calibration card is needed.
 - (4) High acceptance mode will increasing accepting rate, however, it will reduce the security level of Bill Acceptor.